



Group Art Unit: 2133  
Examiner: To be assigned

Applicant(s) : I-Ming LIN et al.

Serial No. : 10/614,040

Filed : July 8, 2003

For: : Signal testing of integrated circuit chip

Attorney Ref: TOP 296

## STATUS REQUEST

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Sir:**

Please let us know the status of the above-identified application and when an Action may be expected. If the response to this status request is made by telephone, please ask for Tonya Harris (Docket Clerk) at extension 518.

Respectfully submitted,

Steven M. Robin

**Steven M. Rabin**  
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January 13, 2005

Date \_\_\_\_\_

SMR:dj

**FEE ENCLOSED: \$0**  
**Please charge any further**  
**fee to our Deposit Account**  
**No. 18-0002**